



Signal & Power Integrity Engineer

Company: Global Engineering Services

Location: Gulberg 3, Lahore, Pakistan

Education: BS. Electrical Engineering Technology (major Electronics) and BS. Tech (major Electronics)

Working Hours: 9:00 AM to 6:00 PM (Monday to Friday)

Position Overview:

We are currently seeking a dedicated Signal & Power Integrity Engineer to join our team. As a Signal & Power Integrity Engineer, you will play a crucial role in ensuring the security, reliability, and robustness of our hardware systems, contributing to the success of our projects

Job Description:

- Good idea of Schematic reading, design flow, BOM etc.
- Able to read & extract the relevant data from electronics components datasheets
- Understanding of EMC/EMI & RF signals
- Investigate the simulation results, dig out the reasons & provide solutions
- Develop test procedures for Signal Integrity testing
- Pre & Post layout simulation
- Generate comprehensive reports detailing simulation results and recommendations for improvement
- Simulate effects of crosstalk on signal integrity
- Integrate IBIS models to accurately simulate component behavior in the design environment
- Experience in the Signal & Power Integrity field shall be preferred
- Experience of EDA tools will be plus point
- Good idea of PCB layout tools
- Knowledge of high-speed digital interfaces such as DRAM/PCIe/SerDes/DDR etc.
- Reworking PCBs
- Excellent communication skills

Salary: Market competitive with incentives like Health & Life insurance, Provident Fund, EOBI, Mobile & Internet allowance, and Annual Bonus.

Apply: hr@gespk.com